

**IN THE CLAIMS:**

Please amend the claims as follows.

Claim 1 (Cancelled)

Claim 2 (Currently Amended): An image sensing apparatus characterized by comprising:  
a plurality of photodetectors;

resettable integrators which are arranged for said respective photodetectors, integrate  
charges output from said photodetectors, and output voltage signals corresponding to integrated  
charge amounts;

output switches arranged on output sides of said respective integrators to connect said  
integrators to an external output line;

a first switch series-inserted between each photodetector and each integrator; and  
a controller for closing said first switch when an absolute value of an output voltage from  
said integrator is lower than a predetermined reference voltage, and opening said first switch  
when the absolute value of the output voltage from said integrator is not lower than the  
predetermined reference voltage

~~An apparatus according to claim 1~~, characterized in that said apparatus further comprises  
a second switch for connecting each photodetector and an overflow drain for removing charges  
output from said photodetector, and

said controller opens said second switch when the absolute value of the output voltage  
from said integrator is lower than the predetermined reference voltage, and closes said second  
switch when the absolute value of the output voltage from said integrator is not lower than the  
predetermined reference voltage.

Claim 3 (Currently Amended): An image sensing apparatus characterized by comprising:  
a plurality of photodetectors;  
resettable integrators which are arranged for said respective photodetectors, integrate  
charges output from said photodetectors, and output voltage signals corresponding to integrated  
charge amounts;  
output switches arranged on output sides of said respective integrators to connect said  
integrators to an external output line;  
a first switch series-inserted between each photodetector and each integrator; and  
a controller for closing said first switch when an absolute value of an output voltage from  
said integrator is lower than a predetermined reference voltage, and opening said first switch  
when the absolute value of the output voltage from said integrator is not lower than the  
predetermined reference voltage

~~An apparatus according to claim 1,~~ characterized in that said apparatus further comprises:  
a third switch series-inserted between each integrator and each output switch; and  
a fourth switch for connecting a terminal of said output switch on the integrator side and  
a supply source for supplying the predetermined reference voltage, and  
said controller closes said third switch and opens said fourth switch when the absolute  
value of the output voltage from said integrator is lower than the predetermined reference  
voltage, and opens said third switch and closes said fourth switch when the absolute value of the  
output voltage from said integrator is not lower than the predetermined reference voltage.